

100

Network

**Figure 1**

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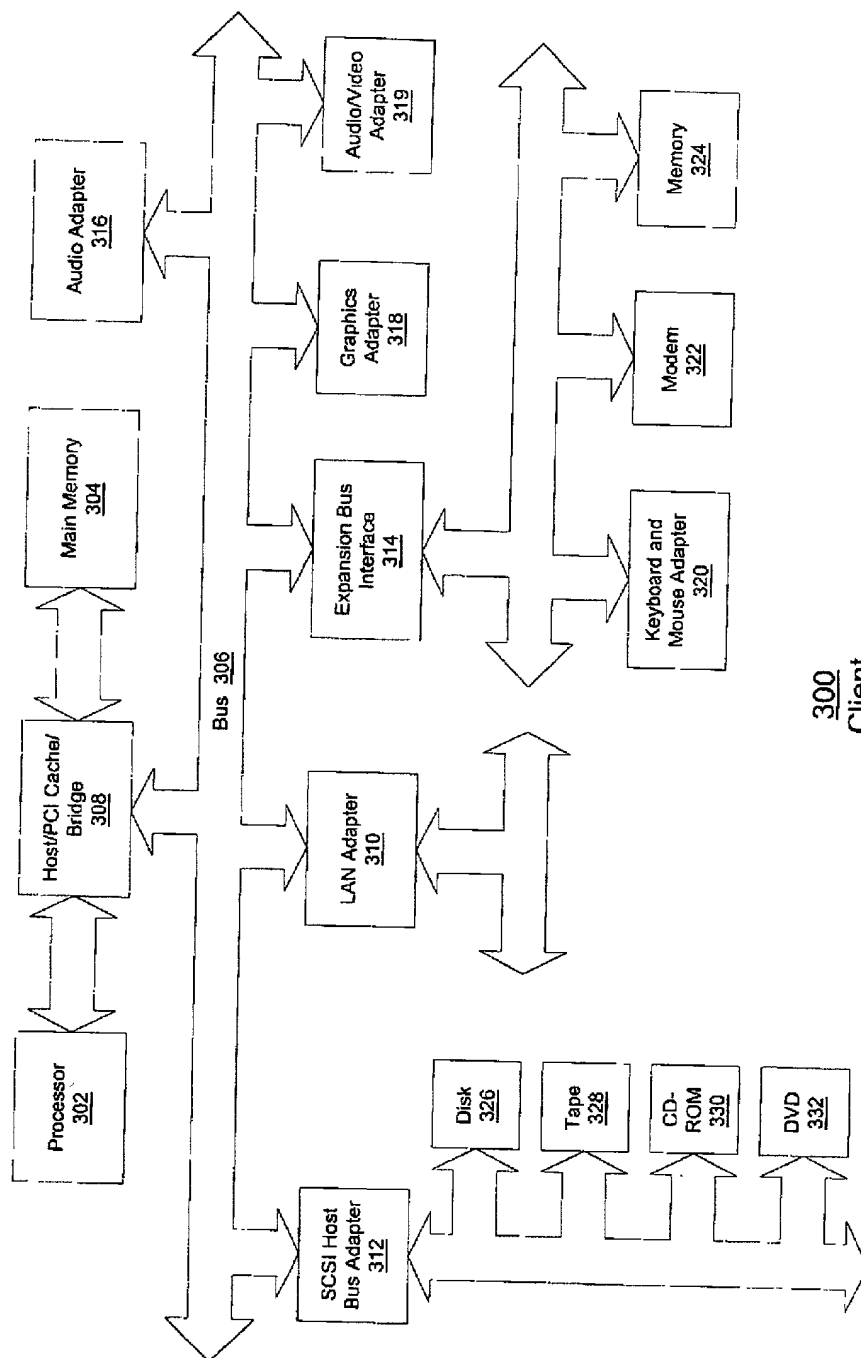
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The diagram illustrates a computer system architecture. At the top, two processors, **Processor 202** and **Processor 204**, are connected to a horizontal **System Bus 206**. Below the system bus is a block containing a **Memory Controller/Cache 208** and an **I/O Bridge 210**. The memory controller/cache is connected to **Local Memory 209**. The I/O bridge is connected to a vertical **I/O Bus 212**. This bus connects to several peripheral devices: a **Graphics Adapter 230** and a **Hard Disk 232** are connected to the left side of the bus. On the right side, three **PCI Bus Bridges** are connected: **PCI Bus Bridge 214** is connected to a **Modem 218** and a **Network Adapter 220** via a **PCI Bus 216**; **PCI Bus Bridge 222** is connected to a **PCI Bus 226**; and **PCI Bus Bridge 224** is connected to a **PCI Bus 228**.

## Figure 2

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**Figure 3**  
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Client

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graph TD; 401[Business decomposition 401] --> 402[Technical decomposition 402]; 402 --> 403[Build BSM configuration database 403]; 403 --> 404[Deploy required monitors to the BP enabling technology 404]; 404 --> 405[Map IT severity to Business Impact severity 405]; 405 --> 406[Develop correlation logic and rules 406]; 406 --> 407[Develop business process views 407]; 407 --> 408[Develop end-to-end Event Management platform 408];
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Business decomposition  
401

Technical decomposition  
402

Build BSM configuration database  
403

Deploy required monitors to the BP enabling technology  
404

Map IT severity to Business Impact severity  
405

Develop correlation logic and rules  
406

Develop business process views  
407

Develop end-to-end Event Management platform  
408

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